SS5817 THRU SS5819

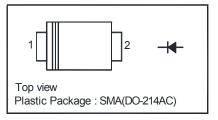
Surface Mount Schottky Barrier Rectifiers Reverse Voltage - 20 to 40 V Forward Current - 1 A

Features

- Plastic package has Underwriters Laboratory Classification 94V-0
- · Metal silicon junction, majority carrier conduction
- · For surface mount applications
- Guard ring for overvoltage protection
- · Low power loss, high efficiency
- High current capability, Low forward voltage drop
- · High surge capability

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Mechanical Data

 Case: SMA (DO-214AC) molded plastic case
 Terminals: Solder plate, solderable per MIL-STD -750, method 2026

· Polarity: Color band denotes cathode end

• Mounting Position: Any

Maximum Ratings and Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, resistive or inductive load, for capacitive load, derate by 20%

Capacitive load, derate by 20% Parameter Symbols SS5817 SS5818 SS5819 Units						
Parameter		SS5817	SS5818	SS5819	Units	
Maximum Repetitive Peak Reverse Voltage		20	30	40	V	
Maximum RMS Voltage		14	21	28	V	
Maximum DC Blocking Voltage		20	30	40	V	
Maximum Average Forward Rectified Current		1			Α	
Peak Forward Surge Current 8.3mS Single Half Sine-wave Superimposed on Rated Load (JEDEC Method)		25			Α	
Maximum Instantaneous Forward Voltage at 1 A		0.45	0.55	0.6	V	
Maximum Instantaneous Reverse Current at T _A = 25°C		0.5			mA	
at Rated DC Blocking Voltage at T _A =100°C	I _R	10			mA	
Typical Junction Capacitance 1)	СЈ	110			pF	
Typical Thermal Resistance ²⁾		75			°C/W	
Operating Temperature		125			°C	
Storage Temperature Range		- 55 to + 150			°C	

¹⁾ Measured at 1 MHz and reverse voltage of 4 V.D.C.



 $^{^{2)}}$ P.C.B. mounted with 2" X 2" (5 X 5 cm) copper pad areas.

Electrical characteristic curves

Fig.1 Forward Current Derating Curve

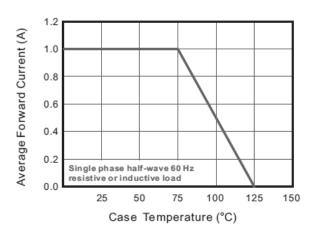


Fig.3 Typical Forward Characteristic

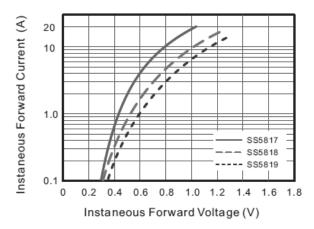


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

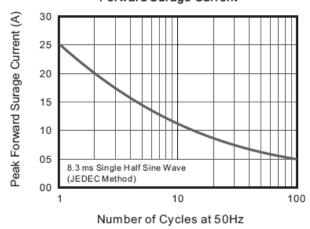


Fig.2 Typical Reverse Characteristics

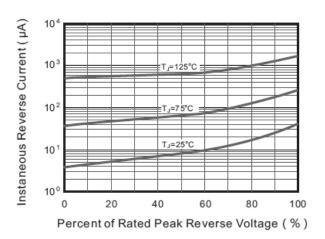


Fig.4 Typical Junction Capacitance

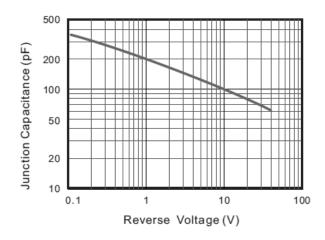
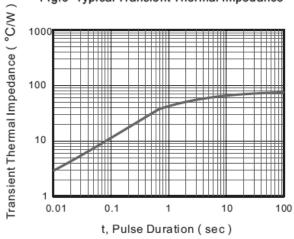


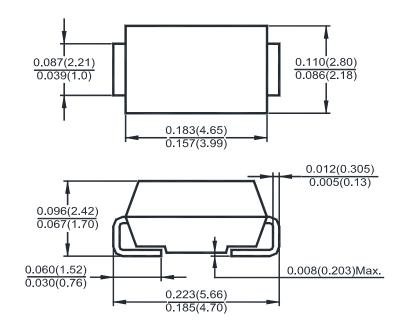
Fig.6- Typical Transient Thermal Impedance





Package Outline Dimensions in inches (millimeters)

SMA(DO-214AC)



Marking information

" ***** " = Part No.

" III " = Cathode line Font type: Arial

