# SK52C THRU SK5AC

## SCHOTTKY BARRIER RECTIFIER Reverse Voltage - 20 to 100 V Forward Current - 5 A

## Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- · Metal silicon junction, majority carrier conduction
- For surface mount applications
- Low power loss, high efficieny
- · High current capability, low forward voltage drop
- · Low profile package
- · Built-in strain relief, ideal for automated placement
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

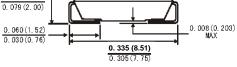
## **Mechanical Data**

- Case: JEDEC SMC (DO-214AB) molded plastic body
- Terminals: solder plated, solderable per MIL-STD-750, method 2026
- · Polarity: color band denotes cathode end

## **Maximum Ratings and Electrical Characteristics**

0. 128 (3. 25) 0. 108 (2. 75) 0. 103 (2. 62) 0. 079 (2. 00)

SMC(DO-214AB)



Dimensions in inches and (millimeters)

Parameter	Symbols	SK52C	SK53C	SK54C	SK55C	SK56C	SK58C	SK5AC	Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	20	30	40	50	60	80	100	V
Maximum RMS Voltage	V <sub>RMS</sub>	14	21	28	35	42	57	71	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	20	30	40	50	60	80	100	V
Maximum Average Forward Rectified Current 0.375"(9.5mm) Lead Length	I <sub>F(AV)</sub>	5							А
Peak Forward Surge Current, 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC method at Rated $T_L$ )	I <sub>FSM</sub>	150							A
Maximum Forward Voltage at 5 A <sup>1)</sup>	V <sub>F</sub>	0.55			0.75 0		85	V	
Maximum DC Reverse Current $T_a = 25 \circ C$ at Rated DC Blocking Voltage $T_a = 100 \circ C$	I <sub>R</sub>	0.5							mA
Typical Junction Capacitance 3)	CJ	500 400						pF	
Typical Thermal Resistance <sup>2)</sup>	R <sub>θJA</sub> R <sub>θJL</sub>	55 10							°C/W
Operating Junction Temperature Range	TJ	- 65 to + 125							°C
Storage Temperature Range	T <sub>Stg</sub>	- 65 to + 150							°C

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, resistive or inductive load. For

<sup>1)</sup> Pulse test: 300 µs pulse width, 1% duty cycle

<sup>2)</sup> P.C.B mounted 0.55 X 0.55" (14X14mm) copper pad areas

<sup>3)</sup> Measured at 1 MHz and applied reverse voltage of 4 V



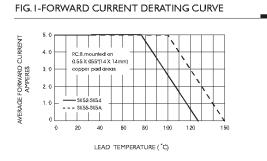
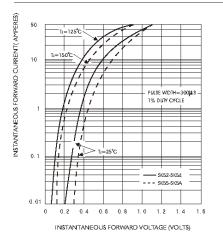
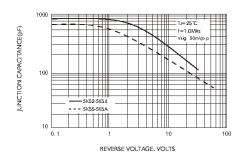


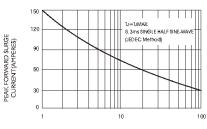
FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS



#### FIG.5-TYPICAL JUNCTION CAPACITANCE



#### FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT



NUMBER OF CYCLES AT 60Hz

FIG.4-TYPICAL REVERSE CHARACTERISTICS

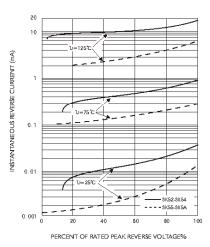
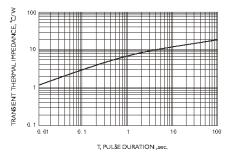


FIG.6-TYPICAL TRANSIENT THERMAL IMPEDANCE





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