# SF101CT THRU SF108CT

### **GLASS PASSIVATED SUPER FAST RECTIFIER**

Reverse Voltage - 50 to 600 V Forward Current - 10 A

#### **Features**

- Low forward voltage drop
- Low reverse leakage current
- · Superfast switching time for high efficiency
- High current capability
- High surge current capability
- High reliability

### **Mechanical Data**

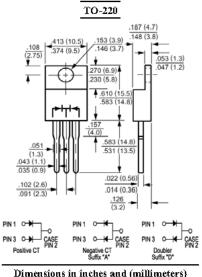
• Case: Molded plastic, TO-220

• Epoxy: UL 94V-0 rate flame retardant

• Terminals: leads solderable per MIL-STD-202

method 208 guaranteed

• Polarity: As marked Mounting Position: Any



Dimensions in inches and (millimeters)

## **Absolute Maximum Ratings and Characteristics**

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

Parameter	Symbols	CE101CT	CE102CT	CE102CT	CE104CT	CE10ECT	SF106CT	CE107CT	SF108CT	Units
Parameter	Symbols	SFIUICI	3F 102C1	SF 103C1	SF 104C1	3F 105C1	3F100C1	3F 107C1	3F 108C1	Office
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	50	100	150	200	300	400	500	600	V
Maximum RMS Voltage	$V_{RMS}$	35	70	105	140	210	280	350	420	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	150	200	300	400	500	600	V
Maximum Average Forward Rectified Current at $T_C = 100$ °C	I <sub>(AV)</sub>	10								А
Peak Forward Surge Current, 8.3 mS Single half Sine-wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	125								Α
Maximum Forward Voltage at 5 A and 25 °C	V <sub>F</sub>	0.95 1.3 1.7					.7	V		
$ \begin{array}{ll} \mbox{Maximum Reverse Current} & \mbox{at T}_{\mbox{\scriptsize A}} = 25 \ ^{\circ}\mbox{\scriptsize C} \\ \mbox{at Rated DC Blocking Voltage} & \mbox{\scriptsize T}_{\mbox{\scriptsize A}} = 100 \ ^{\circ}\mbox{\scriptsize C} \\ \end{array} $	I <sub>R</sub>	10 500								μA
Typical Junction Capacitance 1)	Сл	70 50					50		pF	
Maximum Reverse Recovery Time 2)	t <sub>rr</sub>	35 50						ns		
Typical Thermal Resistance 3)	$R_{\theta JC}$	3								°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	- 55 to + 150								°C

Measured at 1 MHz and applied reverse voltage of 4 VDC.



Reverse recovery test conditions:  $I_F = 0.5 \text{ A}$ ,  $I_R = 1 \text{ A}$ ,  $I_{RR} = 0.25 \text{ A}$  Thermal resistance from Junction to case per leg mounted on heatsink.

FIG.1- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

