

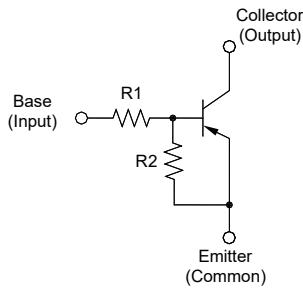
RA107S...RA109S

PNP Silicon Epitaxial Planar Transistor

for switching, interface circuit and drive circuit applications

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



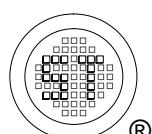
1. Emitter 2. Collector 3. Base
TO-92 Plastic Package

Resistor Values

Type	R1 (KΩ)	R2 (KΩ)
RA107S	10	47
RA108S	22	47
RA109S	47	22

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Output Voltage	$-V_O$	50	V
Input Voltage	RA107S	- 30, 6	V
	RA108S	- 40, 7	
	RA109S	- 40, 15	
Output Current	$-I_O$	100	mA
Total Power Dissipation	P_{tot}	200	mW
Junction Temperature	T_j	150	°C
Storage Temperature Range	T_{stg}	- 55 to + 150	°C

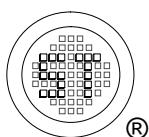


RA107S...RA109S

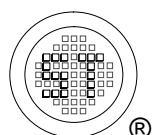
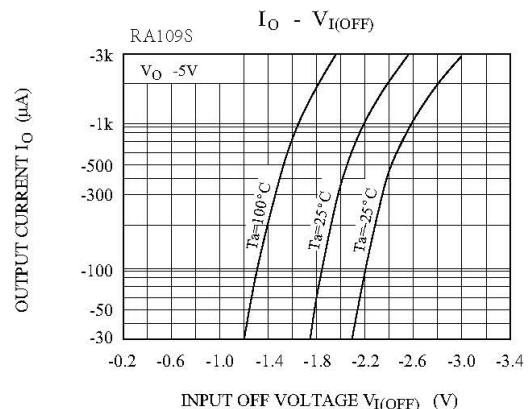
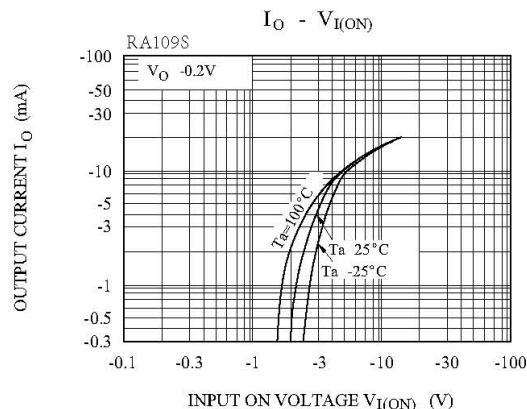
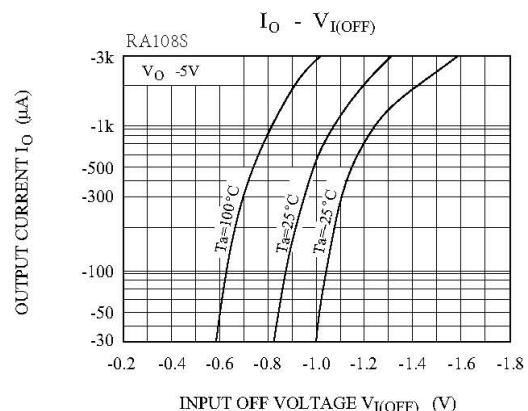
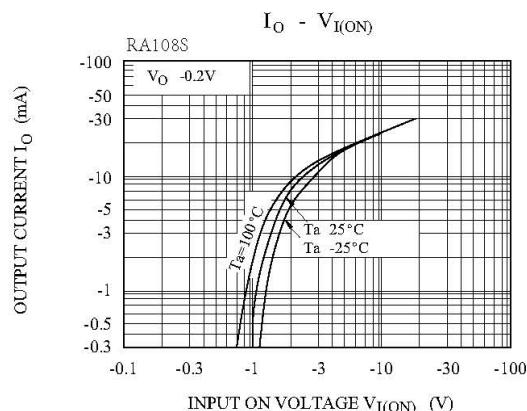
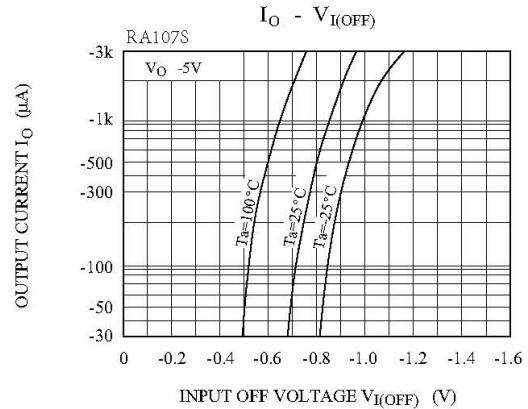
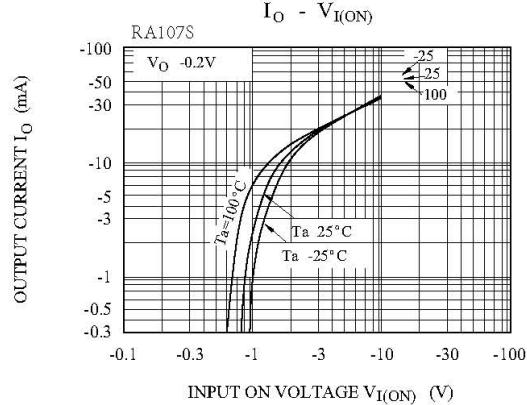
Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $-V_O = 5 \text{ V}$, $-I_O = 10 \text{ mA}$	G_I	80	-	-	-
		RA107S	80	-	-
		RA108S	70	-	-
Output Cutoff Current at $-V_O = 50 \text{ V}$	$-I_{O(\text{OFF})}$	-	-	500	nA
Input Current at $-V_I = 5 \text{ V}$	$-I_I$	-	-	0.88	mA
		RA107S	-	-	
		RA108S	-	-	
Output Voltage at $-I_O = 10 \text{ mA}$, $-I_I = 0.5 \text{ mA}$	$-V_{O(\text{ON})}$	-	-	0.3	V
Input Voltage (ON) at $-V_O = 0.2 \text{ V}$, $-I_O = 5 \text{ mA}$	$-V_{I(\text{ON})}$	-	-	1.8	V
		RA107S	-	-	
		RA108S	-	-	
Input Voltage (OFF) at $-V_O = 5 \text{ V}$, $-I_O = 0.1 \text{ mA}$	$-V_{I(\text{OFF})}$	0.5 0.6 1.5	- - -	-	V
Transition Frequency at $-V_O = 10 \text{ V}$, $-I_O = 5 \text{ mA}$	$f_T^{(1)}$	-	200	-	MHz

¹⁾ Characteristic of transistor only.



RA107S...RA109S



RA107S...RA109S

