

E5V0VBPD1A

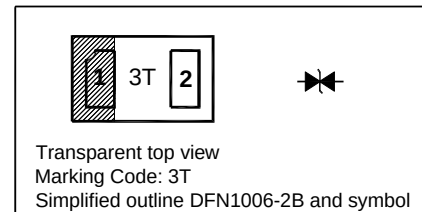
ESD Protection Diode

Features

- Low clamping voltage
- Low operating voltage
- Bi-direction high reliability

PINNING

PIN	DESCRIPTION
1	Anode
2	Anode

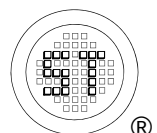


Absolute Maximum Ratings (at $T_a = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp = 8/20 μs)	P_{PK}	100	W
Peak Pulse Current (tp = 8/20 μs)	I_{PP}	4	A
IEC61000-4-2 (ESD) Air Contact	V_{ESD}	± 25 ± 23	KV
Operating Temperature Range	T_j	- 55 to + 125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Max.	Unit
Reverse Stand-Off Voltage	V_{RWM}	-	5	V
Reverse Breakdown Voltage at $I_R = 1 \text{ mA}$	$V_{(BR)R}$	6	11	V
Reverse Current at $V_{RWM} = 5 \text{ V}$	I_R	-	0.1	μA
Clamping Voltage at $I_{PP} = 1 \text{ A}$, tp = 8/20 μs at $I_{PP} = 4 \text{ A}$, tp = 8/20 μs	V_C	- -	13 25	V
Junction Capacitance at $V_R = 0 \text{ V}$, f = 1 MHz	C_j	-	0.65	pF



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Electrical characteristic curves

Fig 1. Pulse Waveform

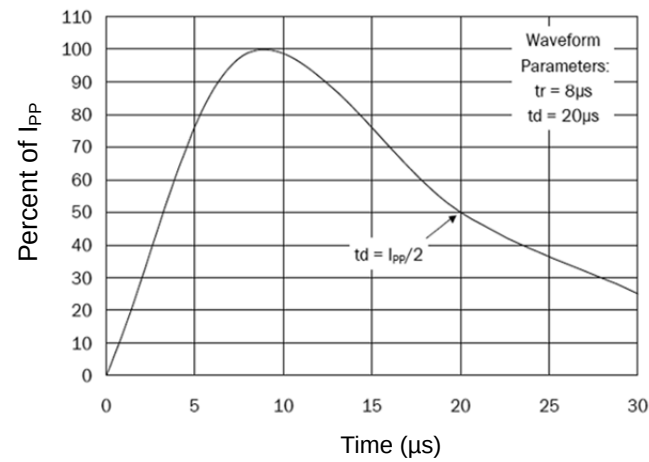


Fig 2. Power Derating Curve

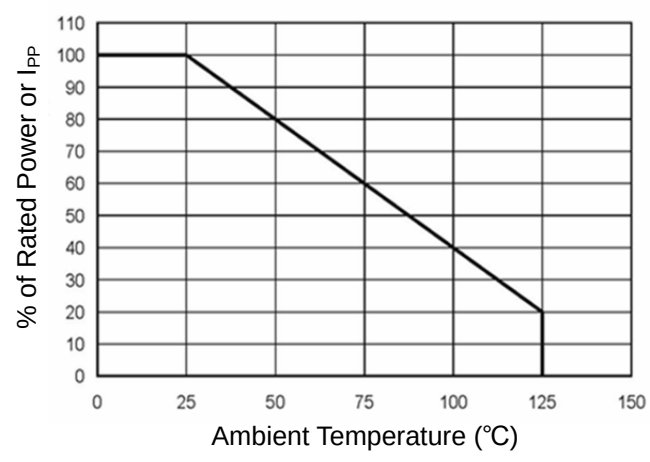


Fig 3. Clamping Voltage Curve

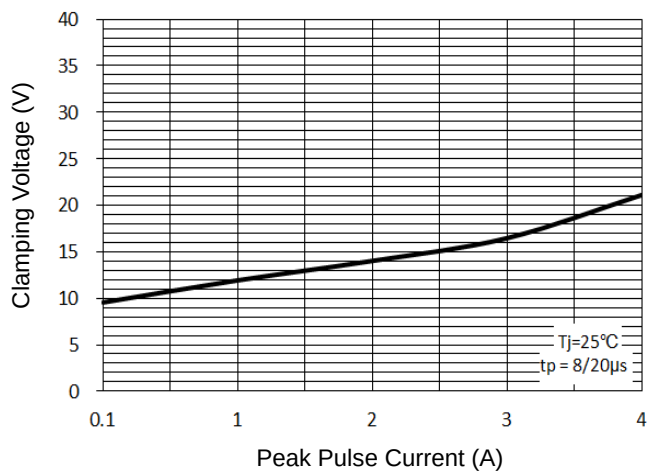


Fig 4. Junction Capacitance

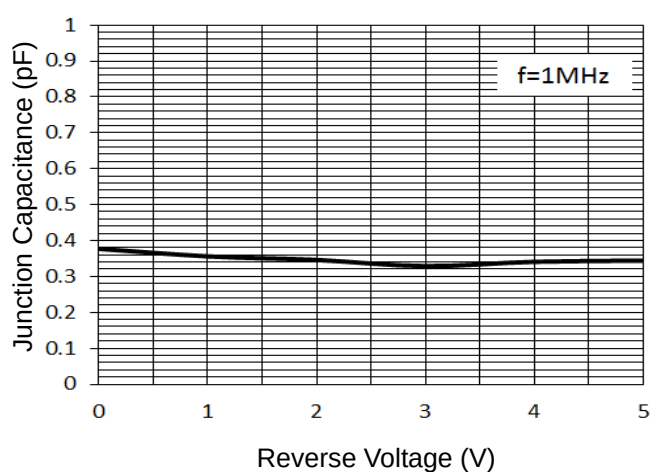
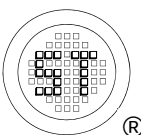
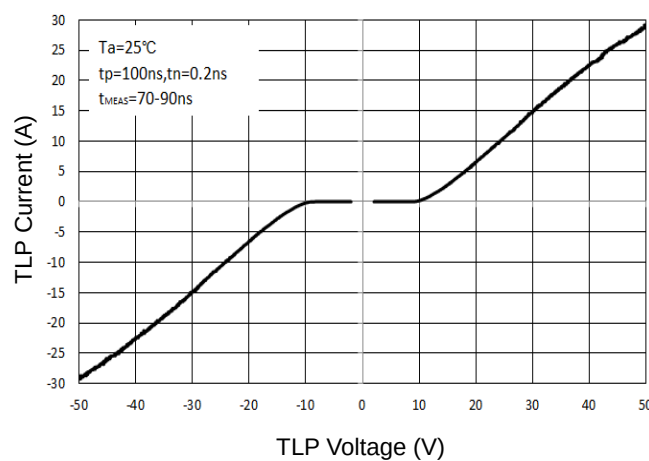


Fig 5 TLP Curve

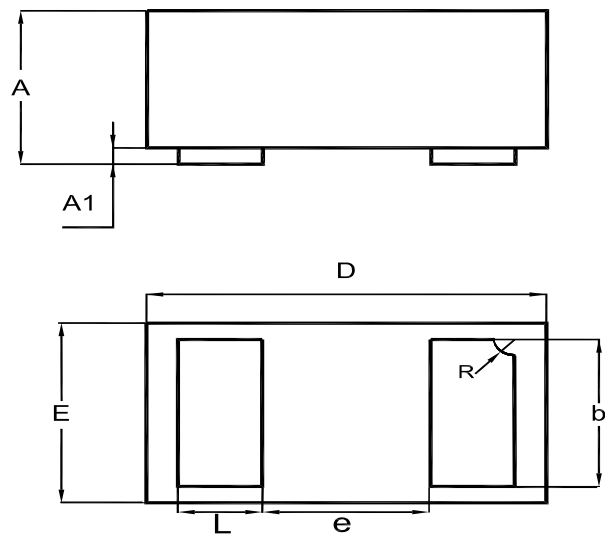


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PACKAGE OUTLINE

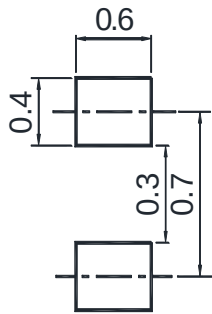
Plastic surface mounted package; 2 leads

DFN1006-2B



UNIT	A	A1	b	D	E	e	L	R
mm	0.40 0.36	0.05 0	0.55 0.45	1.05 0.95	0.65 0.55	0.4	0.3 0.2	0.15 0.05

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN1006-2B	8	2 ± 0.1	0.079 ± 0.004	178	7	10,000

Marking information

" 3T " = Part No.

Font type: Arial

