

DBL201S THRU DBL207S

SINGLE-PHASE GLASS PASSIVATED SILICON SURFACE MOUNT BRIDGE RECTIFIER

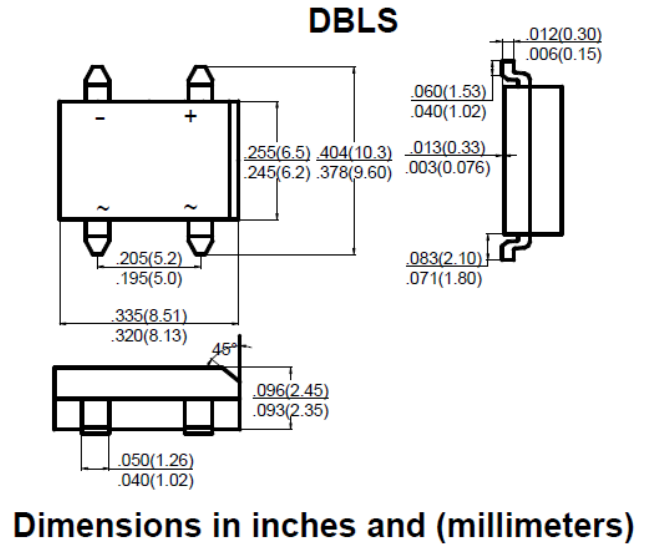
Reverse Voltage - 50 to 1000 V
Forward Current - 2 A

Features

- High surge overload rating of 60 A peak
- Ideal for printed circuit board
- Low forward voltage drop
- Glass passivated chip junction

Mechanical Data

- Case: Molded plastic, DBLS
- Epoxy: UL 94V-0 rate flame retardant
- Terminal: Leads solderable per MIL-STD-202, method 208 guaranteed
- Mounting position: Any



Maximum Ratings and Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Symbols	DBL201S	DBL202S	DBL203S	DBL204S	DBL205S	DBL206S	DBL207S	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at $T_A = 40^\circ\text{C}^{(2)}$	$I_{(AV)}$	2							A
Peak Forward Surge Current 8.3 ms Single Half-sine wave Superimposed on Rated Load (JEDEC Method)	I_{FSM}	60							A
Maximum Forward Voltage at 2 A DC	V_F	1.1							V
Maximum Reverse Current at Rated DC Blocking Voltage	I_R	10							μA
Typical Thermal Resistance	$R_{\theta JA}$	68							$^\circ\text{C/W}$
Typical Thermal Resistance	$R_{\theta JL}$	15							$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_j, T_{stg}	- 55 to + 150							$^\circ\text{C}$

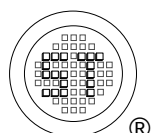


FIG1:Io-Ta Curve

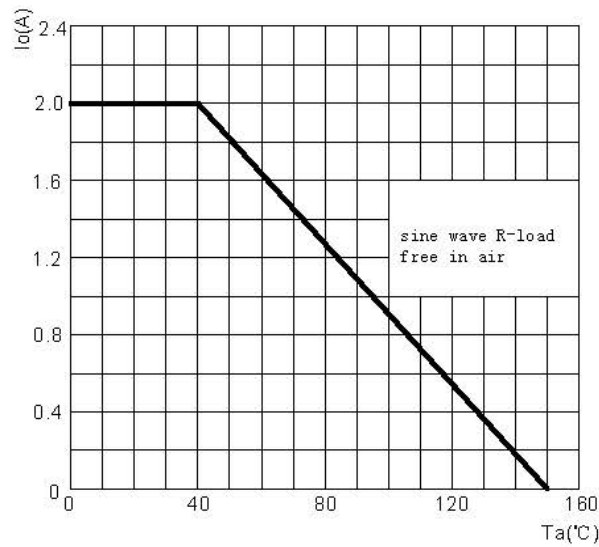


FIG2:Surge Forward Current Capability

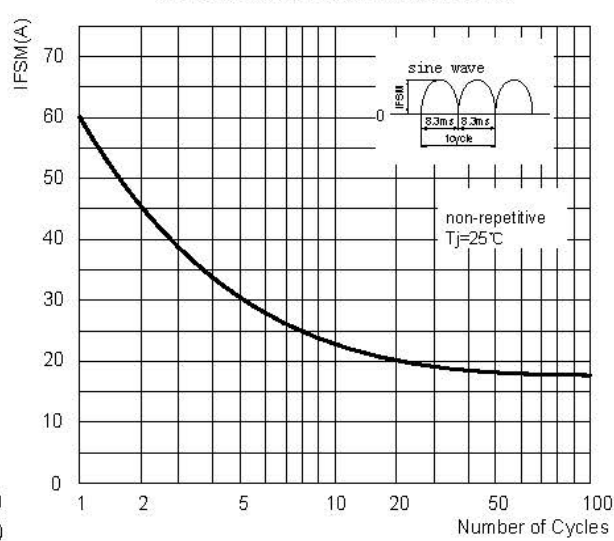


FIG3: Forward Voltage

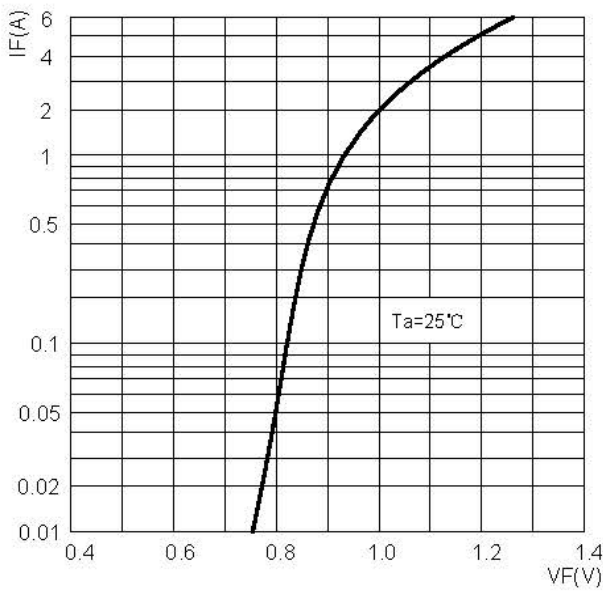


FIG4:Typical Reverse Characteristics

