

BAT721WS-BL

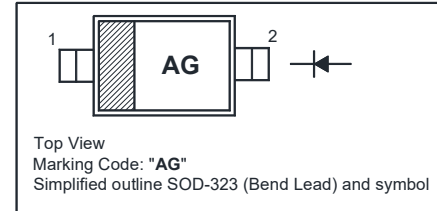
Silicon Epitaxial Planar Schottky Barrier Diode

Features

- Low forward voltage
- Low capacitance

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

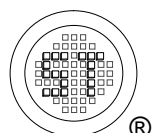


Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Reverse Voltage	V_R	40	V
Average Forward Current	$I_{F(AV)}$	200	mA
Non-repetitive Peak Forward Surge Current (8.3 ms)	I_{FSM}	1	A
Junction Temperature	T_j	125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 65 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Typ.	Max.	Unit
Forward Voltage at $I_F = 10\text{ mA}$ at $I_F = 100\text{ mA}$ at $I_F = 200\text{ mA}$	V_F	- - -	300 420 550	mV
Reverse Current at $V_R = 30\text{ V}$ at $V_R = 30\text{ V}, T_j = 100^\circ\text{C}$	I_R	- -	15 3	μA mA
Diode Capacitance at $V_R = 0\text{ V}, f = 1\text{ MHz}$	C_d	40	50	pF



Electrical Characteristics Curves

Fig 1. Diode Capacitance vs Reverse Voltage

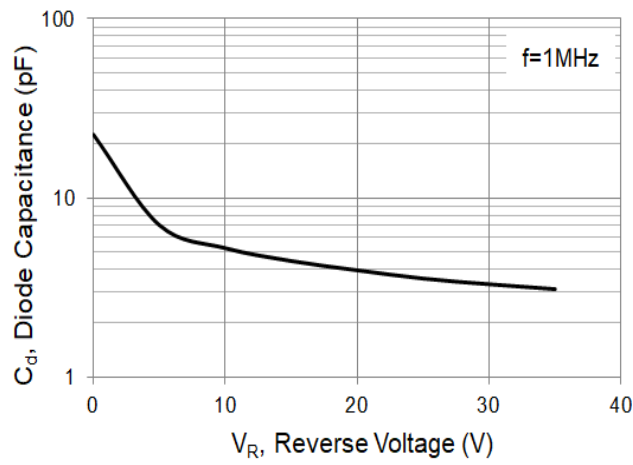


Fig 2. Reverse Current vs Reverse Voltage

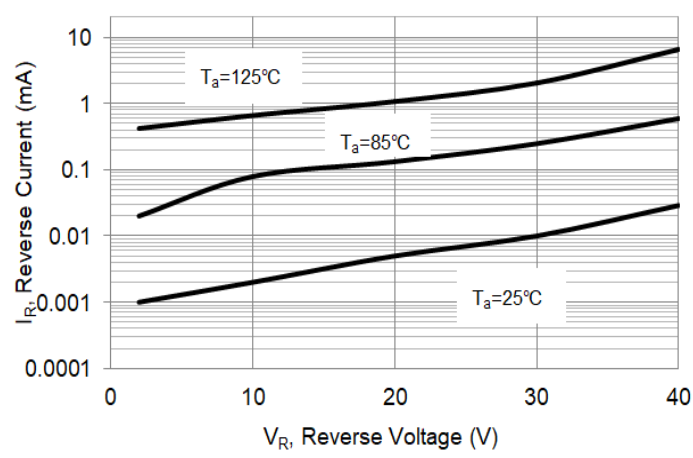
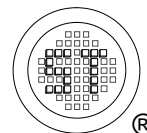
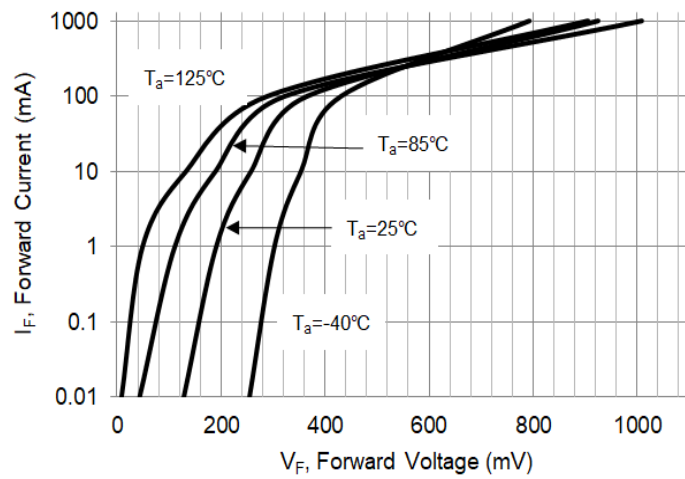


Fig 3. Forward Current vs Forward Voltage

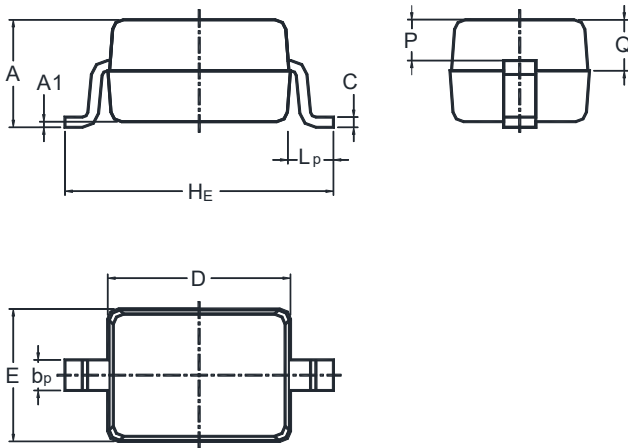


BAT721WS-BL

PACKAGE OUTLINE

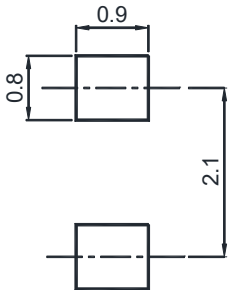
Plastic surface mounted package; 2 leads

SOD-323(Bend Lead)



UNIT	A	A ₁	b _p	C	D	E	H _E	L _p	Q	P
mm	1.1 0.8	0.1 0	0.4 0.25	0.18 0.09	1.8 1.6	1.35 1.15	2.8 2.3	0.5 0.1	0.5 0.3	0.4 0.3

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOD-323BL	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

" III " = Cathode line

" AG " = Part No.

Font type: Arial

